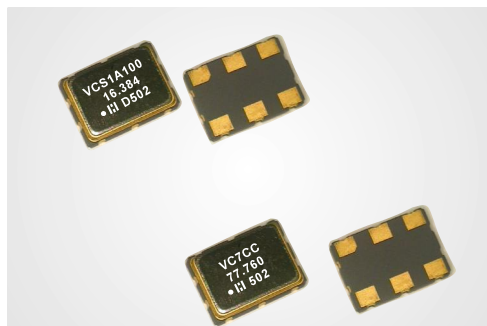


• D7SV Series 7.0*5.0 VCXO

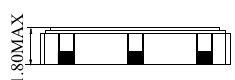
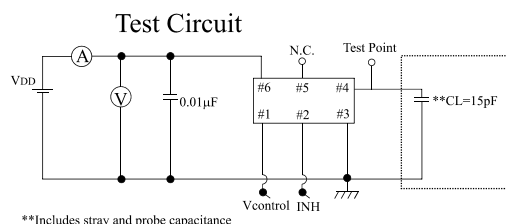
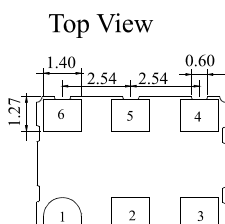
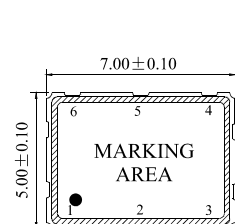


FEATURES

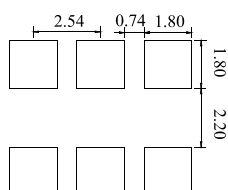
- Industry Standard with 7.0*5.0*1.8mm package
- TTL/HCMOS output compatible
- Tri-State Enable/Disable
- Tight tolerance performance with voltage IC control
- Designed primarily for use in phase locked loops, phase shift keying and other telecommunication applications such as ADSL, set-top box, and base stations etc.

Electrical Specifications

Parameter	Condition	D7SV		
Frequency Range*	F0	1.75~54MHz 50~200MHz		
Frequency Calibration	At 25°C	± 15ppm		
Temperature Stability	Over T _{OPR}	± 15ppm, ± 25ppm, ± 50ppm		
Stability vs. power change	V _{DD} +/-5%	± 5ppm		
Stability vs. load change	15pF+/-10%	± 3ppm		
Pullability	Over Control Voltage Range	± 100ppm, ± 200ppm	± 100ppm, ± 150ppm	
Control Voltage Range		0.5~4.5V	0~3.3V	0~3.3V
Operating Temperature Range	T _{OPR}	-20°C~+70°C (-40°C~+85°C option)		
Storage Temperature Range	T _{STG}	-55°C~+125°C		
Power Supply Voltage	V _{DD}	5.0V ± 5%	3.3V ± 5%	3.3V ± 5%
Aging (First Year)	25°C ± 3°C	± 2ppm		
Supply Current	I _{DD}	30mA Max		40mA Max
Output Symmetry	Sym	40/60%(45/55% Option)		
Rise time	T _r	20%V _{DD} ~80%V _{DD}	8nS Max	10nS Max
Fall Time	T _f	80%V _{DD} ~20%V _{DD}	8nS Max	10nS Max
Output Voltage	V _{OH}	90% V _{DD} min		
	V _{OL}	10% V _{DD} max		
Output Load		15pF Max		
Start-up Time	T _s	10mS Max		
Pin 2,tri-state function		Pin 2=H or open....Output active at pin 4		
		Pin 2=L.....high impedance at pin 4		
Packing Unit		1000pcs/reel		

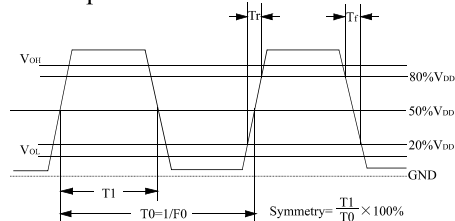


Recommended Solder Pattern



Pin	Connection
#1	V _{control}
#2	Tri-state
#3	GND
#4	Output
#5	N.C.
#6	V _{DD}

Output Waveform



***note: A 0.01µF bypass capacitor should be placed between V_{DD}(Pin6) and GND(Pin3) to Minimize power supply line noise